JSS Mahavidyapeetha
Sri Jayachamarajendra College of Engineering
Dept. of E&EE, SJCE, Mysuru :: Time Table :: Effective from 02-Jan-2017

<table>
<tr><th colspan="12">II SEMESTER</th></tr>
<tr><th>Day/ Time</th><th>07:30 to 08:30</th><th>08:30 to 09:30</th><th>09:30 to 10:30</th><th>10:30 to 11:00</th><th>11:00 to 11:50</th><th>11:50 to 12:40</th><th>12:40 to 1:30</th><th>1:30 to 2:30</th><th>2:30 to 3:30</th><th>3:30 to 4:30</th><th>4:30 to 5:30</th></tr>
<tr><td>Mon</td><td>EE210 ‘E’ MHS
EE210 ‘C’ KMP</td><td>EE210 ‘B’ BAS</td><td>EE210 ‘G’ DSN</td><td rowspan="7">TEA BREAK</td><td></td><td></td><td>EE210 ‘D’ KMV</td><td rowspan="7">LUNCH BREAK</td><td></td><td></td><td></td></tr>
<tr><td>Tue</td><td></td><td>EE210 ‘C’ KMP</td><td>EE210 ‘F’ SN</td><td></td><td>EE210 ‘D’ KMV
EE210 ‘G’ DSN</td><td>EE210 ‘E’ MHS</td><td>EE210 ‘A’ UR</td><td></td><td></td><td colspan="2">EC21L-‘E’ SEC SR+KMP+NSG</td></tr>
<tr><td>Wed</td><td></td><td>EE210 ‘A’ UR
EE210 ‘D’ KMV</td><td>EE210 ‘F’ SN
EE210 ‘G’ DSN</td><td>EE210‘B’ BAS</td><td></td><td></td><td colspan="4">EC21L-‘E’ SEC SR+UR</td></tr>
<tr><td>Thu</td><td></td><td>EE210 ‘G’ DSN</td><td></td><td>EE210 ‘F’ SN</td><td>EE210 ‘E’ MHS</td><td>EE210‘B’ BAS</td><td></td><td></td><td></td><td></td><td></td></tr>
<tr><td>Fri</td><td>EE210 ‘D’ KMV</td><td>EE210 ‘A’ UR</td><td></td><td>EE210‘B’ BAS</td><td></td><td></td><td></td><td></td><td></td><td></td><td>EE210 ‘C’ KMP</td></tr>
<tr><td>Sat</td><td></td><td></td><td></td><td>EE210 ‘C’ KMP</td><td></td><td>EE210 ‘A’ UR
EE210 ‘E’ MHS
EE210 ‘F’ SN</td><td></td><td></td><td></td><td></td><td></td></tr>
</table>

Subject and subject code	Sections	Staff	Room no.
EE210 Basic Electrical Engineering	A	Miss. Usha R	NPS001
	B	Sri. B Aprameya Swarup	NPS001
	C	Mr. Kishore M P	PS006
	D	Mr. Vinay K M	PS006
	E	Dr. M H Sidram	PS203
	F	Mrs. Sowmyashree N	PS204
	G	Mrs. Deepa S N	PS202
EE21L-Basic Electronics lab	E		

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ROOM NO. : EV108

IV SEMESTER

Day/ Time	07:30 to 08:30	08:30 to 09:30	09:30 to 10:30	10:30 to 11:00	11:00 to 11:50	11:50 to 12:40	12:40 to 1:30	1:30 to 2:30	2:30 to 3:30	3:30 to 4:30	4:30 to 5:30
Mon	MA410	EE420 SR	EE430 ADS	**TEA BREAK**	EE46L-B1 BAS+KMP+NSG			**LUNCH BREAK**			
					EE47L-B2 SN+UR						
					EE440L-B3 KMV+MN						
Tue	EE440 KMV	EE410 RSA	EE450 DSN		EE46L-B2 BAS+UR+NSG						
					EE47L-B3 SN+RBS						
					EE440L-B4 MN						
Wed		EE420 SR	EE410 RSA		EE440 KMV	EE450 DSN	HU410 CIP				
Thu					EE430 ADS	EE420 SR	MA410		EE46L-B3 BAS+ UR+NSG		
									EE47L-B4 SN+KMP		
									EE440L-B1 KMV+SR		
Fri		HU410 CIP	EE450 DSN		MA410	EE430 ADS	EE410 RSA		EE46L-B4 BAS+UR+NSG		
									EE47L-B1 DSN+MN		
									EE440L-B2 KMV		
Sat	MA410	EE450 DSN	EE420 SR		EE410 RSA	EE440 KMV	EE430 ADS				

Subject code	Subject	Staff
EE410	Network Analysis-II	Sri R S Ananda Murthy
EE420	Signals and Systems	Sri. S Raghavendra
EE430	Field Theory	Dr. A D Srinivasan
EE440	Op-amps and Linear IC's	Mr. Vinay K M
EE450	Transformers &Induction Machines	Mrs. Deepa S N
EE440L	Op-amps and Linear IC's Lab	
EE46L	D.C. Machines and Synchronous Machines Lab	
EE47L	Circuit Simulation and Signal Processing Lab	

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ROOM NO.:EV109

VI SEMESTER

Day/ Time	07:30 to 08:30	08:30 to 09:30	09:30 to 10:30	10:30 to 11:00	11:00 to 11:50	11:50 to 12:40	12:40 to 1:30	1:30 to 2:30	2:30 to 3:30	3:30 to 4:30	4:30 to 5:30
Mon			EE620 KMP	**TEA BREAK**	EE610 MHS	EE630 RBS	EE650 SR	**LUNCH BREAK**	EE67L-B1 MSS+UR		
									EE68L-B2 ADS+RBS+NSG		
									EE664L-B3 RSA+SN		
Tue	EE630 RBS	EE610 MHS	EE640 ADS		EE664 RSA	EE650 SR	EE620 KMP				
Wed			EE630 RBS		EE640 ADS	EE610 MHS	EE620 KMP		EE67L-B2 MHS+DSN		
									EE68L-B3 BAS+RBS+NSG		
									EE664L-B4 RSA+KMP		
Thu	EE620 KMP	EE630 RBS	EE664 RSA		EE67L-B3 DSN+UR						
					EE68L-B4 KMV+ RBS+NSG						
					EE664L-B1 RSA+ KMP						
Fri	EE650 SR	EE640 ADS	EE610 MHS		EE67L-B4 MHS+UR						
					EE68L-B1 KMV+KMP+NSG						
					EE664L-B2 RSA+DSN						
Sat			EE640 ADS		EE610 MHS	EE650 SR	EE664 RSA				

Subject Code	Subject	Staff
EE610	Power System Analysis and Stability-1	Dr. M H Sidram
EE620	Digital Signal Processing	Mr. Kishore M P
EE630	Switchgear and Protection	Mr. Ravishankar B S
EE640	High Voltage Engineering	Dr. A D Srinivasan
EE650	Control Systems-II	Sri S Raghavendra
EE664	Digital System Design using HDL	Sri R S Ananda Murthy
EE664L	Digital System Design using HDL Lab	
EE67L	Control Systems Lab	
EE68L	Relay and High Voltage Lab	

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ROOM NO.:EV110

VIII SEMESTER											
Day/ Time	**07:30 to 08:30**	**08:30 to 09:30**	**09:30 to 10:30**	**10:30 to 11:00**	**11:00 to 11:50**	**11:50 to 12:40**	**12:40 to 1:30**	**1:30 to 2:30**	**2:30 to 3:30**	**3:30 to 4:30**	**4:30 to 5:30**
Mon				**TEA BREAK**				**LUNCH BREAK**			
Tue											
Wed											
Thu											
Fri	EE833 MN	EE833 MN	EE822 SN		EE810 RBS	EE810 RBS	EE822 SN				
Sat	EE822 SN	EE822 SN	EE810 RBS		EE833 MN	EE833 MN	EE810 RBS				

Subject code	**Subject**	**Staff**
EE810	Management, Electrical Estimation and Entrepreneurship	Mr. Ravishankar B S
EE822	Renewable Energy Systems	Mrs. Sowmyashree N
EE833	Power System Operation and Control	Miss. Neethi M