

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

I Semester

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E E B R E A K	11.00 – 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30- 05.30
Mon	B1	C1				D1 B1	E1 A1				
Tues	A2		C2						B2		
Wed		D2 E2					P1 F2			C3	
Thurs	C4				B3	P2	D3				
Fri					A3	E3 F3	P3				
Sat	D4	A4	P4 E4		F4	B4					

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

III SEM A Sec

ROOM: EV 204

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30		11.00 - 11.50	11.50 – 12.40	12.40 – 01.30		02.30 – 03.30	03.30 – 04.30	04.30 – 05.30
Monday		EC 320L A1 KMP+CBN EC 340L A2 MGVSBR EC 31L A3 KMK+EAM		C O F F E B R E A K	EC 320 UBM	EC 340 NS	EC 350 CRN	L U N C H B R E A K	EC 340L NS		
Tuesday	EC 320L A2 KMP+SR EC 340L A3 NSP+KMK EC 31L A4 MS+YSD		EC 330 MSP		EC 310 VN	EC 320 UBM	EC 350 CRN				
Wednesday		EC 350 CRN	MA 310		HU 320	EC 310 VN	EC 340 NS		EC 320L UBM		
Thursday		MA 310	HU 320		EC 330 MSP	EC 350 CRN	EC 310 VN		EC 330T MSP		
Friday	MA 310	EC 31L SPK	EC 320 UBM		EC 320L A3 UBM+KMP EC 340L A4 MSP+YSD EC 31L A1 MSV+HMR				EC 310T VN		
Saturday	EC 330 MSP	MA 310	EC 340 NS		EC 320L A4 UBM+KMP EC 340L A1RMV+KMK EC 31L A2 EAM+CBN						

MA310: Maths
EC 330: Sensors and Actuators
EC31L: Simulation Lab

EC 310: Circuit Theory
EC 340: Digital Electronic Circuits

EC 320: Analog Electronic Circuits
EC 350: Principles of Communication Systems

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSORE – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

III SEM B Sec

Room: AB 104

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30	
Monday	EC 350 CRN	MA 310	EC 310 VN		EC 320L A1 HMR+NSP EC 340L A2 MGVS+SBR EC 31L A3 KMP+EAM					EC 340L NS		
Tuesday		EC 350 CRN	EC 320 UBM		EC 330 NSP	MA 310	EC 340 MGV			EC 320L A2 UBM+DRP EC 340L A3 MSP+MGV EC 31L A4 CBN+EAM		
Wednesday		EC 320L A3 NSP+KMP EC 340L A4 MGVS+MSP EC 31L A1 MS+EAM			EC 320 UBM	MA 310	EC 330 NSP			EC 320L UBM		
Thursday	EC 31L SR	EC 330 NSP	EC 340 MGV		EC 320L A4 UBM+CBN EC 340L A1 MGVS+SBR EC 31L A2 RMV+EAM					EC 310T VN		
Friday	HU 320	EC 350 CRN	EC 310 VN		EC 340 MGV	MA 310				EC 330T NSP		
Saturday			EC 320 UBM		EC 310 VN	EC 350 CRN	HU 320					

MA310: Maths
EC 330: Sensors and Actuators
EC31L: Simulation Lab

EC 310: Circuit Theory
EC 340: Digital Electronic Circuits

EC 320: Analog Electronic Circuits
EC 350: Principles of Communication Systems

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

V SEM A Sec

Room: NPS 001

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30
Monday	EC 59L TS	EC 510 GHM	EC 550 MNJ		EC 520 BASK	EC 540 SPK	EC 560 RMV		EC 57L A1(2.30-4.30) BSR+HMR EC 58L A2 MSV+KMK EC 59L A3 TS+ASP		
Tuesday		EC 530 BSR	EC 540 SPK		EC 550 MNJ	EC 520 BASK	EC 510 GHM		EC 57L BSR		
Wednesday		EC 560 RMV	EC 530 BSR		EC 57L A2(11.00-12.40) SBR+ASP EC 58L A3 GHM+MS EC 59L A4 MG+V+DRP				EC 58L GHM		
Thursday	EC 57L A3(8.30-10.30) HMR+MSP EC 58L A4 TRM+EAM EC 59L A1KMP+KMK				EC 520 BASK	EC 510 GHM	EC 550 MNJ				
Friday	EC 57L A4(8.30-10.30) HMR+SR EC 58L A1 MSP+RMV EC 59L A2 CBN+MS				EC 530 BSR	EC 560 RMV	EC 540 SPK		EC 520 T DRP		
Saturday	EC 540T MS		EC 550 MNJ		EC 510 GHM	EC 530 BSR	EC 560 RMV				

EC 510:Analog comm.
EC 540:Control Systems
EC 57L: Microprocessor lab

EC 520:Digital Signal Processing
EC 550: Microwaves and Antenna
EC 58L : Analog Commn lab

EC 530:Microprocessors
EC 560: Data structures and Algorithms
EC 59L: DSP Lab

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

V SEM B Sec

Room NPS002

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30	
Monday	EC 59L TS	EC 550 MNJ	EC 560 RMV		EC 530 BSR	EC 540T MS						
Tuesday	EC 560 RMV	EC 510 GHM	EC 550 MNJ		EC 57L B1(11.00-12.40) SBR+HMR EC 58L B2 MSP+CBN EC 59L B3 TS+KMP				EC 57L BSR			
Wednesday	EC 560 RMV	EC 510 GHM	EC 540 SPK		EC 520 BASK	EC 550 MNJ	EC 530 BSR		EC 58L GHM			
Thursday		EC 520 T DRP			EC 530 BSR	EC 540 SPK	EC 560 RMV		EC 57L B2(2.30-4.30) HMR+KMK EC 58L B3 GHM+EAM EC 59L B4 KMP+ASP			
Friday			EC 510 GHM		EC 550 MNJ	EC 520 BASK	EC 530 BSR		EC 57L B3(2.30-4.30) KMK+MSP EC 58L B4 RMV+EAM EC 59L B1 KMP+YSD			
Saturday	EC 57L B4(8.30-10.30) BSR+HMR EC 58L B1 TRM+EAM EC 59L B2 MGV+DRP				EC 540 SPK	EC 520 BASK	EC 510 GHM					

EC 510:Analog comm.
EC 540:Control Systems
EC 57L: Microprocessor lab

EC 520:Digital Signal Processing
EC 550: Microwaves and Antenna
EC 58L : Analog Commn lab

EC 530:Microprocessors
EC 560: Data structures and Algorithms
EC 59L: DSP Lab

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

VII Sem A Sec

ROOM: EV 205

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30	
Monday						EC 71L A1 KMK+MNJ EC 710L A2 MSP+SR				ELECTIVE CRN EV 204		
Tuesday		EC 71L A2 ASP+EAM EC 710L A3 SBR+HMR										
Wednesday	EC 71L A3 KMK+CBN EC 710L A4 HMR+YSD		EC 720 KMK		EC 731 TS	EC 710 HMR	EC 744 CRN					
Thursday	EC 741 YSD	EC 744 CRN	EC 741 YSD		EC 731 TS	EC 720 KMK	EC 710 HMR			EC 71L MNJ EV 205		
Friday	EC 731 TS	EC 720 KMK	EC 741 YSD		EC 744 CRN	EC 71L A4 GHM+CBN EC 710L A1 MS+KMK				EC 710L HMR EV 205		
Saturday	EC 744 CRN	EC 741 YSD	EC 720 KMK		EC 731 TS	EC 710 HMR						

EC 710: VLSI Circuits

EC 741: Automotive Electronics

EC 720: Optical Fiber Comm.

EC 744: IOT

EC 731 : Image Processing

EC 71L: Advanced Commn Lab

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

VII Sem B Sec

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30
Monday		EC 71L B1MS+YSD EC 710L B2 MSP+SR							ELECTIVE CRN EV 204		
Tuesday									EC 71L B2SR+KMK EC 710L B3 SBR+HMR		
Wednesday		EC 731 TS AB 104	EC 710 SR AB104		EC 720 MSV NPS001	EC 741 KMP NPS001	EC 744 CRN NPS001		EC 71L B3 CBN+MSP EC 710L B4 SBR+HMR		
Thursday		EC 744 CRN NPS 001	EC 731 TS NPS 001		EC 741 KMP AB 104	EC 720 MSV AB 104	EC 731 TS AB 104		EC 71L MNJ EV 205		
Friday		EC 720 MSV NPS 001	EC 741 KMP NPS 001		EC 744 CRN EV 204	EC 710 SR EV 204	EC 731 TS EV 204		EC 710L HMR EV 205		
Saturday	EC 744 CRN NPS 002	EC 720 MSV NPS 002	EC 741 KMP NPS 002		EC 710 SR EV 204	EC 71L B4 MNJ+ASP EC 710L B1 SBR+MSP					

EC 710: VLSI Circuits
EC 741: Automotive Electronics

EC 720: Optical Fiber Commn
EC 744: IOT

EC 731: Image Processing
EC 71L: Advanced Commn Lab

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

M Tech in Industrial Electronics

AB 102

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30
Monday			LIE 120 NMR		110M1 ASP	LIE 141 DRP	110M2 VN		LIE 141L DRP		
Tuesday		110M2 TS	LIE 130 BASK		LIE 141 DRP	LIE 120 NMR	LIE 153 SG		LIE 16L NMR		
Wednesday			110M1 ASP		LIE 120 NMR	LIE 153 SG	LIE 130 BASK				
Thursday			LIE 153 SG		LIE 141 DRP	LIE 130 BASK	LIE 120 NMR		LIE 130L BASK		
Friday		110M1 ASP	LIE 120 NMR		LIE 153 SG	LIE 141 DRP	LIE 130 BASK		LIE 153L SG		
Saturday											

110M1: Linear Algebra
LIE 141: Advanced DSP
110M2: PCRW

LIE 120: Power Electronics
LIE 153: Advances in VLSI

LIE 130: Embedded Systems
LIE 16L: IE LAB1

(Head of the Department)

(Principal)

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSORE – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

M Tech in Networking And Internet Engg.

AB 203

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30	C O F F E B R E A K	11.00 - 11.50	11.50 – 12.40	12.40 – 01.30	L U N C H B R E A K	02.30 – 03.30	03.30 – 04.30	04.30 – 05.30
Monday			LNI 120 NSP		LNI 151 MSV	LNI 130 TRM	110M2 VN		LNI 120L NSP		
Tuesday		110M2 TS	LNI 130 TRM		LNI 141 NS	110M1 TRM	LNI 151 MSV		LNI 130L TRM		
Wednesday			LNI 151 MSV		LNI 141 NS	LNI 120 NSP	110M1 TRM				
Thursday			LNI 151 MSV		LNI 130 TRM	LNI 120 NSP	LNI 141 NS		LNI 141L NS		
Friday			110M1 TRM		LNI 141 NS	LNI 130 TRM	LNI 120 NSP		LNI 151L MSV		
Saturday					LNI 16L MSV						

LNI 120: Adv. Comm Networks
LNI 151: Storage Area Networks
LNI 16L: LNI LAB1

LNI 130: Cryptography
110 M1: OOPS

LNI 141: Network Programming
110 M2: PCRW

(Head of the Department)

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SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING: MYSURU – 570 006

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ODD SEMESTER 2017-18

MTech in Automotive Electronics

AB 106

Day	07.30 – 08.30	08.30 – 09.30	09.30 – 10.30		11.00 - 11.50	11.50 – 12.40	12.40 – 01.30		02.30 – 03.30	03.30 – 04.30	04.30 – 05.30
Monday			MAL 130 SPK	C O F F E	110M1 ASP	MAL 120 YSD	110M2 VN	L U N C H	MAL 141T SR		
Tuesday		110M2 TS	MAL 120 YSD		MAL 151 KSB	MAL 130 SPK	MAL 141 SR		MAL 120T YSD		
Wednesday			110M1 ASP		MAL 120 YSD	MAL 151 KSB	MAL 141 SR		MAL 16L SR		
Thursday			MAL 130 SPK	B R E A K	MAL 141 SR	MAL 151 KSB	MAL 120 YSD	B R E A K	MAL 130T MS		
Friday		110M1 ASP	MAL 130 SPK		MAL 141 SR	MAL 151 KSB					
Saturday											

110M1: Linear Algebra
MAL 130: Digital Control Systems
MAL 16L: AE LAB1

110M2: PCRW
MAL 141: Advanced Embedded Systems

MAL 120: DADS
MAL 151: Vehicle Safety

(Head of the Department)

(Principal)

