

+DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE FOR ACADEMIC YEAR 2018-19 (ODD SEMESTER) - UG

Semester: III

Section: ----

Room. No. : EV109

TIME	7.30 to 8.30	8.30 to 9.30	9.30 to 10.30	10.30 to 11.00	11.00 to 11.50	11.50 to 12.40	12.40 to 1.30	1.30 to 2.30	2.30 to 3.30	3.30 to 4.30	4.30 to 5.30		
DAY													
MON					EE350 KMV	EE340 RBS	EE320 ADS		EE310 SR	EE310 SR	MA310		
TUE	MA310	EE330 SN	EE320 ADS	T E A B R E A K	EE330L-B1 SN+CM			L U N C H B R E A K					
					EE340L-B2 SR+RBS								
					EE350L-B3 MN+KMV								
WED	MA310	EE310 SR	EE330 SN		HU320 ES	EE330L-B2 SN+CM							
						EE340L-B3 SR+RBS							
						EE350L-B4 MN+KMV							
THU						EE350 KMV	EE320 ADS				EE310 SR	EE330 SN	MA310
FRI	EE310 SR	HU320 ES	EE340 RBS		EE330L-B3 SN+CM								
					EE340L-B4 SR+RBS								
					EE350L-B1 MN+KMV								
SAT	EE340 RBS	EE320 ADS	EE350 KMV		EE330L-B4 SN+CM								
					EE340L-B1 SR+RBS								
					EE350L-B2 MN+KMV								

Sub-Code	Subject Title	Credits	Course Instructor
MA310	Transform Techniques	4	
EE310	Network Analysis	4	S Raghavendra
EE320	D.C. and Synchronous Machines	4	Dr A D Srinivasan
EE330+EE330L	Electrical and Electronic Measurements	4	Sowmyashree N
EE340+EE340L	Logic Design	4	Ravishankar B S
EE350+EE350L	Analog Electronic Devices and Circuits	4	Vinay K M
Total Credits		24	

Head of the Dept.

Coordinators of Time-table committee
 (Prof. P. Prasad & Prof. A.M. Chandrashekar)

Principal
 (Dr. T. N. Nagabhushan)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE FOR ACADEMIC YEAR 2018-19 (ODD SEMESTER) - UG

Semester: V

Section: ----

Room. No. : EV110

TIME	7.30 to 8.30	8.30 to 9.30	9.30 to 10.30	10.30 to 11.00	11.00 to 11.50	11.50 to 12.40	12.40 to 1.30	1.30 to 2.30	2.30 to 3.30	3.30 to 4.30	4.30 to 5.30	
DAY												
MON		EE530L-B3 RSA+CM		T E A B R E A K	EE540 MHS	EE550 BAS	EE510 CM	L U N C H B R E A K	EE56L-B1 BAS+MN+KMV			
									EE57L-B2 MHS+RBS+CM			
									EE530L-B4 RSA+SN			
TUE		EE530L-B1 RSA+RBS			EE550 BAS	MA510	EE520 SR		EE56L-B3 RSA+DSN+DBP			
									EE57L-B4 MHS+CM			
									EE550L-B2 BAS+KMV			
WED		EE550L-B1 BAS+DBP			EE520 SR	EE530 RSA	EE540 MHS					
THU		EE550L-B3 BAS+DBP		EE510 CM	MA510	EE520 SR	EE56L-B4 RSA+DSN+DBP					
							EE57L-B1 MHS+CM					
							EE550L-B2 BAS+RBS					
FRI		MA510	EE510 CM	EE540 MHS	EE530 RSA	EE520 SR	EE56L-B2 BAS+DBP+DSN					
							EE57L-B3 ADS+ SN+CM					
							EE530L-B4 RSA+MN					
SAT	EE510 CM	MA510	EE520 SR	EE550 BAS	EE530 RSA	EE540 MHS						

Sub-Code	Subject Title	Credits	Course Instructor
MA510	Numerical Methods of Computation	4	
EE510	Power Electronics	4	Chaithra M
EE520	Control Systems-I	4	S Raghavendra
EE530+EE530L	Microcontrollers	4	R S Ananda Murthy
EE540	Electrical Power Transmission and Distribution	4	Dr M H Sidram
EE550+EE550L	Technical Communication and Professional Ethics	4	B Aprameya Swarup
EE56L	Transformers and Induction Machines Lab	1.5	
EE57L	Power Electronics Lab	1.5	
Total Credits		27	

Head of the Dept.

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Principal
 (Dr. T. N. Nagabhushan)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE FOR ACADEMIC YEAR 2018-19 (ODD SEMESTER) - UG

Semester: VII

Section: ----

Room. No. : EV109/EV110

TIME	7.30 to 8.30	8.30 to 9.30	9.30 to 10.30	10.30 to 11.00	11.00 to 11.50	11.50 to 12.40	12.40 to 1.30	1.30 to 2.30	2.30 to 3.30	3.30 to 4.30	4.30 to 5.30
DAY											
MON				T E A B R E A K		EE730L-B1 DBP+DSN		L U N C H B R E A K	EE710 DSN	EE730 MSS	EE720 DBP
TUE					EE76L-B4 DSN+DBP+KTV		EE741 MHS		EE756 RBS	EE756 RBS	EE730 MSS
WED	EE756 RBS	EE710 DSN	EE741 MHS EE742 MN		EE76L-B3 DSN+DBP+KTV						
THU	EE720 DBP	EE710 DSN	EE756 RBS		EE741 MHS EE742 MN	EE730L-B2 MSS+DSN+DBP					
FRI	EE76L-B2 DBP+CM+KTV		EE720 DBP		EE710 DSN	EE730L-B3 MSS+DSN+DBP					
SAT	EE76L-B1 DSN+SN+KTV		EE741 MHS EE742 MN		EE730 MSS	EE730L-B4 DBP+DSN					

Sub-Code	Subject Title	Credits	Course Instructor
EE710	Power System Analysis and Stability-2	4	Deepa S N
EE720	Industrial Drives and Applications	3	Divyashree B P
EE730+EE730L	Electrical Machine Design	4	Dr M S Shashikala
EE741/ EE742	Digital Image Processing/ Fuzzy Logic Systems	4	Dr M H Sidram/ Neethi M
EE756	Testing and Commissioning of Electrical Equipment	4	Ravishankar B S
EE76L	Power System Simulation Lab	1	
Total Credits		20	

Head of the Dept.

Coordinators of Time-table committee
 (Prof. P. Prasad & Prof. A.M. Chandrashekar)

Principal
 (Dr. T. N. Nagabhushan)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

TIME TABLE FOR ACADEMIC YEAR 2018-19 (ODD SEMESTER) - UG

Sem: I Sem

TIME	7.30 to 8.30	8.30 to 9.30	9.30 to 10.30	10.30 to 11.00	11.00 to 11.50	11.50 to 12.40	12.40 to 1.30	1.30 to 2.30	2.30 to 3.30	3.30 to 4.30	4.30 to 5.30
DAY											
MON				T E A B R E A K				L U N C H B R E A K			
TUE			EE110-A KMV EE110-P MN				EE110-B SN EE110-E BAS				
WED	EE110-A KMV EE110-P MN				EE110-B SN EE110-E BAS						
THU		EE110-A KMV EE110-P MN				EE110-B SN EE110-E BAS					
FRI			EE110-A KMV EE110-P MN				EE110-B SN EE110-E BAS				
SAT											

Sub. Code	Subject Title	Section	Credits	Course Instructor	Room No
EE110	Elements of Electrical and Electronics Engineering	A	4	Vinay K M	PS201
EE110	Elements of Electrical and Electronics Engineering	B	4	Sowmyashree N	PS201
EE110	Elements of Electrical and Electronics Engineering	E	4	B Aprameya Swarup	PS203
EE110	Elements of Electrical and Electronics Engineering	P	4	Neethi M	PS203