



JSS
SCIENCE AND
TECHNOLOGY
UNIVERSITY
MYSURU

- Constituent College of JSS Science and Technology University
- Approved by A.I.C.T.E
- Governed by the Grant-in-Aid Rules of Government of Karnataka
- Identified as lead institution for World Bank Assistance under TEQIP Scheme



DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION

TIME TABLE (UG)
(NOVEMBER 2021 TO FEBRUARY 2022)

Date: 15/11/2021

Semester: III

Room. No.:EI 201

DAY \ TIME	7:30-8:30	8:30-9:30	9:30-10:30	10:30-11:00	11:00-11:50	11:50-12:40	12:40-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30		
MON			20EI330 KSB	B R E A K	20EI340 NS	20EI310 KS	20EI320 CHP	B R E A K	20EI37L(B1) NS+VPM				
									20EI38L(B2) KSB+MUA				
									20EI320L(B3) CHP+KS				
TUE			20MA310 BJM		20EI320 CHP	20EI310 KS	20EI340 NS		20EI330 KSB	20HU311 PR	20HU311 PR		
WED						20EI37L(B2) NS+VPM			20MA310 BJM	20EI310(T) KS			
						20EI38L(B3) KUR+KSB							
						20EI320L(B4) CHP+KS							
THURS					A	20EI320 CHP	20EI350 KUR		20EI310 KS	A	20EI37L(B3) NS+VPM		
										20EI38L(B4) KUR+MUA			
FRI		20EI330 KSB	20EI350 KUR		K	20EI37L(B4) NS+KS			K				
						20EI38L(B1) KUR+KSB							
						20EI320L(B2) CHP+VPM							
SAT	20MA310 BJM	20EI340 NS	20EI350 KUR		20EI320L(B1) CHP+VPM								

Sub-Code	Subject Title	Credits	Course Instructor
20MA310	Mathematics III	3:0:0	Mr. B J Manjunath(BJM)
20EI310	Circuit Theory and Analysis	3:1:0	Dr. K. Shailaja (KS)
20EI320	C++ and Data Structures	3:1:0	Ms. Chaya H P (CHP)
20EI330	Transducers and Instrumentation – I	3:0:0	Dr. K. S. Babu (KSB)
20EI340	Analog Electronic Circuits	3:0:0	Dr. Nanda. S (NS)
20EI350	Logic Design	3:0:0	Dr. K Umarani(KUR)
20HU311	Universal Human Values (UHV)	2:0:0	Ms. P. Rajeswari (PR)
20EI37L	Analog Electronic Circuits Lab	0:0:1.5	Dr. Nanda. S (NS)
20EI38L	Logic Design Lab	0:0:1.5	Dr. K Umarani(KUR)
Total Credits		25	

Signature of the HOD
(Dr. K. Shailaja)