JSS MAHAVIDYAPEETHA JSS SCIENCE AND TECHNOLOGY UNIVERSITY

SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING



• Constituent College of JSS Science and Technology University

Approved by A.I.C.T.E

Governed by the Grant-in-Aid Rules of Government of Karnataka Identified as lead institution for World Bank Assistance under TEQIP Scheme



Department of Electronics and Communication Engineering

ANALOG ELECTRONICS /LINEAR INTEGRATED CIRCUITS LABORATORY Room No: AB 201

• Staff In-Charge : Prof Halesh M R, Assistant Professor Prof. Roopa M, Assistant Professor

- Technical / Supporting Staff : B T Mahesh
 Mechanic
 Puneeth Kumar
 Helper
- Facilities Available : CRO-20MHz, Dual Channel , Signal generators- 2MHz with counter and output, Regulated Power supplies- 0-30V, 2A., ±15V, ±5V. Measuring instruments and Electronic Components.
- Utilization :

This lab has state-of-the-art electronic equipment like Digital Storage Oscilloscopes, Large Bandwidth Signal Generators, high quality consumables and workbenches, measuring instruments and components. Facilities in this lab are used for conducting experiments in the subjects Basic Electronics Lab, Analog Electronics Lab, Op-amps and Linear ICs Lab, as prescribed in the curriculum of B.E Degree Program in E&CE. They may also be used for student project works. This lab is useful for students to design and analyze working of integrated circuits .This lab also trains the students in designing and assembling analog electronic and integrated circuits for practical applications.

