### SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING



Constituent College of JSS Science and Technology University

· Approved by A.I.C.T.E

· Governed by the Grant-in-Aid Rules of Government of Karnataka

Identified as lead institution for World Bank Assistance under TEQIP Scheme

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## **Department of Electronics and Communication Engineering**

# COMMUNICATION LAB / DESIGN & IMPLEMENTATION LABORATORY

Room No. AB 202C

• Staff In-Charge: Prof. A Thyagaraja Murthy

Associate Professor **Dr. Gayitri H M**Associate Professor

• Technical / Supporting Staff: Nagaraju M B

Instructor

Naveen Kumar B

Helper

CROs, signal generators, regulated power supplies, Multimeters. Spectrum Analyzer, Function Generator, Microwave Lab Setup, Laser Kit, OFC Kit, Modulation/ Demodulation kits.

#### • Utilization:

This laboratory has state of the art electronic equipment including industry – standard Digital Oscilloscopes and Signal Generators. This lab aims at teaching the students about Linear Integrated Circuit design, the 555 Timer, Analog Communication Systems and implementing them using hardware. It also provides the students with relevant information on the basic concepts of processes circuits and other building blocks of experiments related to digital communication. It presents the basic concept used in circuit design, microwave communication and optical communication and also provides various methods and techniques for conducting the experiment. It provides opportunity for students to design useful projects.

