



DEPARTMENT OF ELECTRONICS AND COMMUNICATION TIME

TABLE FOR ACADEMIC YEAR 2020-21 (OOD SEMESTER) – PG Semester: I

Branch: Industrial Electronics

Day	8.30 – 9.30	9.30 – 10.30	10.30 – 11.00	11.00 – 11.50	11.50 – 12.40	12.40 – 1.30	1.30 – 2.30	2.30 – 3.30	3.30 – 4.30	4.30 – 5.30
Mon			B R E A K	LIE 151	LIE 130	LIE 140	B R E A K	LIE 16L		
Tue				LIE 151	EC PGM11	LIE 130		EC PGM21	LIE 140T	
Wed				LIE 151	LIE 130	LIE 140		LIE 142	LIE 151T	
Thu				LIE 142	LIE 130T			LIE 17S		
Fri		LIE 142		EC PGM21	LIE 140	LIE 151		EC PGM11	LIE 142T	
Sat		EC PGM11		LIE 130	LIE 140	LIE 142				

Sub-Code	Subject Title	Credits	Course Instructor	Initials
ECPGM11	Linear Algebra	3	Prof. Anitha S Prasad	ASP
ECPGM21	Technical report writing and documentation	2	Dr. M G Veena	MGV
LIE 130	Power Electronics	5	Dr. U B Mahadevaswamy	UBM
LIE 140	Embedded Systems	5	Prof. B A Sujathakumari	BASK
LIE142	Mechatronics	5	Prof. Ramya M V	RMV
LIE151	Advances in VLSI	5	Dr. S B Rudraswamy	SBR
LIE 16L	Industrial Electronics Lab -1	1.5	Dr. U B Mahadevaswamy	UBM
LIE 17S	Design and implementation -1	1.5	Prof. Anitha S Prasad	ASP

Head of the Dept.
(Dr. Shankaralab)

Coordinator of Time-table committee
(Dr. U B Mahadevaswamy)

Principal
(Dr. S. B. Kivade)



DEPARTMENT OF ELECTRONICS AND COMMUNICATION TIME

TABLE FOR ACADEMIC YEAR 2020-21 (OOD SEMESTER) – PG Semester: I

Branch: Network and Internet Engineering

Day	8.30 – 9.30	9.30 – 10.30	10.30 – 11.00	11.00 – 11.50	11.50 – 12.40	12.40 – 1.30	1.30 – 2.30	2.30 – 3.30	3.30 – 4.30	4.30 – 5.30	
Mon			B R E A K	LNI 130	LNI 152	LNI 140	B R E A K	LNI 16L			
Tue		LNI 152		LNI 141	LNI 140	EC PGM15		EC PGM21	LNI 141T		
Wed		LNI 141		LNI 152	LNI 140	LNI 130		LNI 17S			
Thu				LNI 140	EC PGM15	LNI 130		LNI 141	LNI 152T		
Fri	LNI 130T			EC PGM21	LNI 140T						
Sat		LNI 130		LNI 141	LNI 152	EC PGM15					

Sub-Code	Subject Title	Credits	Course Instructor	Initials
ECPGM15	Object Oriented Programming	3	Prof. Supreetha M	MS
ECPGM21	Technical report writing and documentation	2	Dr. M G Veena	MGV
LNI 130	Advanced Communication Networks	5	Prof. Kavayshree M K	KMK
LNI 140	Cryptography and Network security	5	Prof. Thyagarajamurthy	TRM
LNI141	Network programming	5	Dr. Shankaraiah	NS
LNI152	Cloud computing and virtualization	5	Prof. M S Vinayprasad	MSV
LNI 16L	Networking Lab-1	1.5	Prof. Kavayshree M K	KMK
LNI 17S	Design and implementation -1	1.5	Prof. Thyagarajamurthy	TRM

Head of the Dept.
(Dr. Shankaraiah)

Coordinator of Time-table committee
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Principal
(Dr. S. B. Kivade)



- Constituent College of JSS Science and Technology University
- Approved by A.I.C.T.E
- Governed by the Grant-in-Aid Rules of Government of Karnataka
- Identified as lead institution for World Bank Assistance under TEQIP Scheme



DEPARTMENT OF ELECTRONICS AND COMMUNICATION TIME

TABLE FOR ACADEMIC YEAR 2020-21 (OOD SEMESTER) – PG Semester: I

Branch: Automotive Electronics

Day	8.30 – 9.30	9.30 – 10.30	10.30 – 11.00	11.00 – 11.50	11.50 – 12.40	12.40 – 1.30	1.30 – 2.30	2.30 – 3.30	3.30 – 4.30	4.30 – 5.30	
Mon			B R E A K	MAL 120	MAL 143	MAL 152	B R E A K	MAL 110T			
Tue		MAL 110		MAL 143	EC PGM11	MAL 120		EC PGM21	MAL 120T		
Wed		MAL 120		MAL 110	EC PGM11	MAL 152		MAL 15L			
Thu		MAL 152		EC PGM11	MAL 110	MAL 143		MAL 120	MAL 152 T		
Fri				EC PGM21	MAL 143	MAL 110		MAL 152	MAL 143 T		
Sat				MAL 16S							

Sub-Code	Subject Title	Credits	Course Instructor	Initials
ECPGM11	Linear Algebra	3	Dr. Shankaraiah	NS
ECPGM21	Technical report writing and documentation	2	Dr. M G Veena	MGV
MAL110	Design of Automotive Dynamics Systems	5	Prof. Madhusudhan M P	MPM
MAL120	Digital Control Systems	5	Dr. Sudarshan Patil Kulkarni	SPK
MAL143	Advanced Embedded Systems	5	Prof. B S Renuka	BSR
MAL152	Vehicle Body Engineering & Safety	5		MECH
MAL 15L	Digital Control Systems Lab	1.5	Dr. U B Mahadevaswamy	UBM
MAL16S	Design and implementation -1	1.5	Prof. Yashwanth S D	YSD

Head of the Dept.
(Dr. Shankaraiah)

Coordinator of Time-table committee
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Principal
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