



- Constituent College of JSS Science and Technology University
- Approved by A.I.C.T.E
- Governed by the Grant-in-Aid Rules of Government of Karnataka
- Identified as lead institution for World Bank Assistance under TEQIP Scheme



Department of Electronics and Communication Engineering

Visual TCAD and Research Lab

Room No. CS202

Staff In-Charge:

Dr. U B Mahadevaswamy

Professor

Prof. Puneeth K M

Assistant Professor

Prof. Chandrashekar murthy B N

Assistant Professor

Technical/Supporting Staff:

Asha G D

Instructor

Shivashankar

Helper

Facilities Available:

- Visual TCAD software installed on three desktops with Intel(R) Core (TM) i5-4460 CPU @ 3.20GHz with 16.0 GB RAM on Linux platform.
- TCAD software with perpetual license.
- The procured TCAD has the facility to design and simulate devices using 10nm CMOS technology. Full-custom Application Specific Integrated Circuits (ASIC) design facility. Front end and Back-end designing is carried out. Complete design flow and various verification are carried out like DRC, LVS and GDSII.

Utilization:

- Research scholars, PG students.
- Consultancy

