Sl No	FACULTY WEBSITE PROFILE	
1	Name of The Faculty	Dr. SIDRAM M.H
2	Designation	Professor
3	Qualification	B.E., M.Tech., Ph.D.
4	E Mail ID	drmhs@jssstuniv.in
5	LinkedIn ID	Sidram M H
6	ORC ID	0009-0009-7725-8142
7	Contact Number	+91 9342187929
8	Work Experience	
	1. Teaching	30
	2. Industry	-
	3. Research	17
	4. Other	-
9	Area of Specialization	Power Electronics, Power systems, Embedded Systems, Image and Video Processing.
10	Subject Taught	
	1. Under Graduate Level	Electrical Power Transmission and Distribution, Power system analysis and stability analysis, Power Electronics, Measurements and Instrumentation, Electrical Machines, Basic Electrical Engineering.
	2. Post Graduate Level	Energy Efficiency in Electrical Utilities and Thermal Systems, General Aspects of Energy Management and Energy Audit.
11	Research Guidance	
	1. Under Graduate Level	Guided several projects at UG Level with International Publications
	2. Post Graduate Level	Guided several projects at PG Level with International Publications
	3. PhD	1. Ms. Saritha M (USN: JSTD17EEE002), enrolled at JSS Science and Technology University, Mysuru, successfully completed her doctoral research titled “Analysis and Design of Advanced PLL for Power Quality Compensators”. She was awarded the PhD degree in December 2025. 2. Ms. Rubeena Banu (USN: 4JC17PEA15), enrolled at Visvesvaraya Technological University, Belagavi, successfully completed her doctoral research titled “Approaches for Object Tracking in Video Surveillance”. She was awarded the PhD degree in April 2024.
12	Publication Details	
	1. Inter National Journal	14
	2. National Journal	-
	3. Conferences	14

13	Patents	1. IOT Based Virtual Students Monitoring System by using TF Plus Mini Lidar Sensor"-published on 1.04.2021 in Australian patent, application no:2021100210. 2. Advanced phase-locked loop (PLL) systems for power quality enhancement in electrical grids" – Published on05.07.2024 in Indian Innovation Patent, Application No: 202441049255 A. 3. PLL Device for Power Quality Improvement Systems" – Design Patent, Indian, Design No: 421397-001.
14	Research Projects / Consultancy Undertaken	
	1. Funding Agency	-
	2. Fund Received (In Rs)	-
15	Book Published	-
16	FDP /STTP/Technical Seminar Organised and Participated as a Resource Person	1. Contributed as Member of the assessment committee to assess grade III & above [technical officers] for promotion on 22/03/2024 2. Participated in Two Week Faculty Development Programme on "Integrated Solid Waste Management (ISWM)" conducted during 22nd August to 3rd September 2005 3. CONTRIBUTED AS MEMBER OF BOARD OF EXAMINERS FOR THE ACADEMIC YEAR 2022-23., ON 19/10/2022 4. Participated in One Week faculty development programme on "Recent Trends in Biomedical Signal & Image Processing" conducted during 10th - 14th March 2016 5. Participated the Workshop on 'Advances in Image Processing and Computer Vision', Conducted during 29th - 31st Jan 2009 6. Contributed as judge/jury member for state level technical project exhibition on 29/06/2022 7. Participated in Two Week Faculty Development Programme on "Recent Trends in Polymer Composites (RTPC)" conducted during 17th - 29th September 2001 8. Participated in Two Week AICTE sponsored Faculty Development Programme on "Soft Computing Techniques for the Engineering Research and its Applications (SCTERA'15)" conducted during 8th -21st

		June, 2015 9. Workshop (ISTE) on ‘Writing Effective Conference Papers’ Conducted during 18th – 19th February, 2012 10. Participated in 3 days’ workshop on “IoT and Applications” conducted during 26th - 28th May 2022 11. Participated in Two-Week AICTE – ISTE Short Term Training Programme on ‘Unix Operating System', Conducted during 4th – 16th September 2000 12. Second International Conference on 'Signal and Image Processing (ICSIP-2009) conducted during 12th - 14th August 2009 13. Participated in One Week Faculty Development Programme on “Research Methodology for Science, Engineering and Management” conducted during 25th – 30th May 2015 14. Participated in One Week (6 days) workshop on “Pedagogy of Instructional Design and Delivery System” from 12th to 17th July 2016, as TEQIP-II program 15. Participated in Two-Week AICTE sponsored Staff Development Programme on ‘Soft skills and Efficiency Enhancement', Conducted during 11th – 23rd July 2005 16. Participated the Workshop on 'Video Analysis and Surveillance', Conducted during 6th -10th July 2010 17. CONTRIBUTED AS JUDGE FOR A NATIONAL LEVEL TECHNICAL SYMPOSIUM FOR UG AND PG STUDENTS HELD ON 30/4/2016 18. Contributed as chairman of board of studies from 31-05-2022 to 11-09-2025 19. Coordinator for one-day workshop on “Stress Management” for women faculty members, ON 28TH FEBRUARY 2019 20. Participated in One Week Workshop on ‘Electronics in Industrial Automation', conducted during 16th - 21st July 2007 21. NATIONAL CONFERENCE ON ‘SCIENTIFIC ACHIEVEMENTS OF SC/ST SCIENTISTS AND TECHNOLOGISTS (NCSCST-2009) conducted during 14th -16th APRIL 2009 22. First National Conference On 'Image Processing, Pattern Recognition and Computer Vision conducted during 24th -

		25th August 2009 23. Contributed member of board of studies (bos) during 2017-2022 24. Coordinator for one Week faculty development programme on “Advances in Pattern Recognition: An Approach through MATLAB” from 31st August 2016 to 4th September 2016 25. Participated in One Week TEQIP-II sponsored Faculty Development Programme on “Sustenance of Energy and Environment: Challenges and Research Dimension” conducted during 25th to 30th July 2016, 26. Participated in One Week “Management Capacity Enhancement Program-me (MCEP) for TEQIP Institutions” conducted during 27th - 31st July ,2015 27. Contributed as Member of Board of Examiners for the Academic year 2016-2017., on 27/8/2016 28. Contributed as judge for VIVACIOUS -16 - A project COMPETITION held on 6/5/2016 29. Contributed as Member of the assessment committee to assess grade III staff [technical officers] for promotion held on 11/09/2019, 30. Participated in Two Week AICTE sponsored Faculty Development Programme on ‘Hands on Course on VLSI Design', Conducted during 16th – 28th August 1999 31. Participated in the One Week Workshop on Digital Image Processing held during 1-5th October 2010 32. Contributed as Subject Expert for Construction Committee (Electrical engineering),26/12/2022 33. Participated in two-week course on “Digital Transformation in Teaching Learning Process” under the banner of NPIU, TEQIP-3, conducted during 6th to 22nd April 2020. 34. Participated in One Week faculty development programme on “Embedded System Design using ARM 7 and TMS320C6713”, conducted during 14th July to 18th July 2014 35. Contributed as Subject Expert for Setting Question Papers on 3/12/2025 36. Participated in Workshop on ‘Financial Management’, Conducted during 30th -31st July 2014 37. Participated in Two Week AICTE sponsored

		Faculty Development Programme on ‘Hands on Course on Spice and Virtual Instrumentation', Conducted during 23rd July – 4th August 2007 38. Contributed as external member of Board of Examination for the Academic Year 2022-2023. On 19/10/2022 39. Participated in One day workshop on Equity Action Plan conducted by SPIU Karnataka in association with NPIU on November 14, 2018 40. Participated in Two Week AICTE sponsored short term course on “Recent Advances in Microwave Engineering” conducted during 16th – 27th March, 2015, 41. Subject Expert for electrical works on 12/2/2024 42. Participated in Workshop on “Energy, Power and Environment (IWEPE-2019)” during March 17th-19th 2019. 43. Contributed as academic council (AC) member f member from 23/12/2024 to 11/09/2025 44. CONTRIBUTED AS MEMBER OF BOARD OF EXAMINERS for post-graduation., ON 10/7/2023 45. Participated in One Week Faculty Development Programme on “Internet of Things (IoT) and Its Applications” conducted during 8th to 12th September 2016. 46. Contributed as Member of the assessment committee to assess grade II staff / technicians for promotion on 9/04/2021 47. CONTRIBUTED AS MEMBER OF BOARD OF EXAMINERS FOR THE ACADEMIC YEAR 2020-21., ON 1/3/2021 48. Participated in One Week faculty development programme on “Hands on Course on FPGA and ASIC Design” conducted during 28th december, 2015 - 1st January 2016 49. Participated in AICTE staff development programme on 'Image and Video Processing', conducted during 23rd Nov - 6th Dec 2009 50. CONTRIBUTED AS MEMBER OF BOARD OF studies (Bos) FOR THE ACADEMIC YEARs 2022-23.,2023-24 and 2024-25. Attended several meetings

		51. Participated in the "Authors Workshop on Publish and Perish ", an initiative under TEQIP II, held on 26th March, 2016. 52. Participated in Two Week AICTE sponsored Faculty Development Programme on 'Advances in Neural Computing', Conducted during 26th July –7th August 1999 53. Participated in One Week faculty development programme on "Nature Inspired Algorithms & Their Applications (NIATA-2015)" Conducted during July 13th - 17th, 2015
17	Achievements	1. Secured First Class with Distinction with 3rd Rank in B.E from Gulbarga Univeristy, Gulbarga 2. Acted as a member of the Board of Studies (BoS) in the Department of Electrical and Electronics Engineering, Vidyavardhaka College of Engineering (VVCE), Mysuru, for the academic year 2021–2022 on 4 October 2021. 3. Acted as a member of the Board of Examiners (BoE) in the Department of Electrical and Electronics Engineering, Vidyavardhaka College of Engineering (VVCE), Mysuru, for the academic year 2021–2022 on 4 October 2021. 4. Acted as a member of the Assessment Committee at CSIR–Central Food Research and Technology Institute (CFTRI), Mysuru, to assess Grade III technical officers for promotion to the next higher grade on 9 April 2021. 5. Acted as a member of the Board of Examiners (BoE) in the Department of Electrical and Electronics Engineering, PES College of Engineering, Mandya, for the academic year 2020–2021 in November 2020. 6. Served as a member of the Assessment Committee at CSIR–Central Food Research and Technology Institute (CFTRI), Mysuru, to assess Grade III technical officers for promotion to the next higher grade. 7. Participated in the staff selection process at NIE Institute of Technology, Mysuru, in August 2019. 8. Conducted remedial classes for academically challenged students across all semesters during May–June 2019.

		9. Acted as a judge for final-year undergraduate project evaluation in the Department of Electrical and Electronics Engineering, Vidhyavardhaka College of Engineering, Mysuru, in 2016. 10. Conducted a one-day workshop on “Stress Management” for women faculty members of JSS Science and Technology University (JSSSTU), Sri Jayachamarajendra College of Engineering (SJCE), Mysuru, on 28 February 2019. 11. Conducted a one-week Faculty Development Programme (FDP) on “Recent Trends in Power Sector for Sustainable Development” from 17 to 21 December 2018, organized by the Department of Electrical and Electronics Engineering, Sri Jayachamarajendra College of Engineering, Mysuru. 12. Conducted a one-week Faculty Development Programme on “Advances in Pattern Recognition: An Approach through MATLAB” from 31 August to 4 September 2016, jointly organized by the Departments of Electrical and Electronics Engineering and Electronics and Communication Engineering, Sri Jayachamarajendra College of Engineering, Mysuru. 13. Evaluated M.Tech. theses and served as an external examiner at various institutions across Karnataka. 14. Acted as a member of the Board of Examiners (BoE) in the Department of Electrical and Electronics Engineering, PES College of Engineering, Mandya, during the academic year 2016–2017. 15. Acted as a judge for a technical symposium organized by the Department of Electrical and Electronics Engineering, Channabasaveshwara Institute of Technology (CIT), Gubbi, Tumakuru. 16. Elected as a class representative and subsequently served as a member of the Student Council during the fourth-year election for the academic year 1992–1993. 17. Delivered several presentations to students on research publications. 18. Served as a moderator for evaluation work at various engineering colleges. 19. Set question papers for other engineering colleges and universities, including

		Visvesvaraya Technological University (VTU) and PES College of Engineering, Mandya.
18	Professional Membership	1. Fellow of Institute of Engineers (FIE), India. 2. Fellow of Institute of Engineers (FIE), India.